

Notice of Allowability	Application No.	Applicant(s)
	10/620,929	ST. CLAIR, LAWRENCE B.
	Examiner Mujtaba K. Chaudry	Art Unit 2133

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to interview on 12/4/2006.
2. The allowed claim(s) is/are 1-7, 10, 15, 16, 21 and 23.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application
6. Interview Summary (PTO-413),
Paper No./Mail Date 12/4/2006.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

Albert Decady
ALBERT DECADY
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100

Examiner's Amendment

An Examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Philip S. Lyren on Monday, December 4, 2006.

Please amend the application as follows:

Please replace claim 1 with:

1. A computing device, comprising:

 a memory for storing data and error detection data associated with the data; and

 a processor coupled to the memory, wherein the processor executes:

 a plurality of platform dependent modules, each platform dependent module-capable of running on a different platform and processing the data,

 a platform detection module to (1) detect a targeted platform on which a data transference package is running and (2) activate one of the plurality of platform dependent modules to run on the targeted platform,

 an error detection selection module that holds a selected error detection scheme, and

an error detection module running on the targeted platform to activate the selected error detection scheme with respect to the data, wherein the targeted platform includes at least one of a targeted operating system (OS) and a targeted computing environment.

Please replace claim 2 with:

2. The computing device of claim 1, wherein the data comprises a file that executes on the targeted platform.

Please replace claim 3 with:

3. The computing device of claim 1, wherein the data transference package comprises an amalgamated data transference package; and wherein the data, the error detection data, the plurality of platform dependent modules, the error detection selection module, the error detection module, and the transference success module are bundled together.

Please replace claim 4 with:

4. The computing device of claim 1, wherein the data transference package comprises a distributed data transference package; and wherein the plurality of platform dependent modules and the error detection module reside at a destination data store, while the data, the error detection data, the error detection selection module, and the transference success module are bundled together and extant at an originating data store and/or on a communication link.

Please replace claim 5 with:

5. The computing device of claim 23, wherein the transference success module provides at least one of: a secondary error check; an equivalency checking with reference to an expected value; installation of a file of the data; retransmission of the file of the data; encryption of the data and/or the data transference package; decryption of the data and/or the data transference package; compression of the data and/or the data transference package; decompression of the data and/or the data transference package; a changing or modifying of information of the data; and an addressing of an error of the data component to ensure that the data is not fatally corrupted and/or to remedy the error.

Please replace claim 6 with:

6. The computing device of claim 1, wherein the error detection module is further adapted to effectuate the selected error detection scheme with respect to the data by being adapted (i) to apply the selected error detection scheme to the data to produce at least one value and (ii) to compare the at least one value to the error detection data.

Please replace claim 7 with:

7. The computing device of claim 1, wherein the plurality of platform dependent modules comprises a collective platform dependent module, and wherein the error detection module comprises a plurality of error detection modules.

Please replace claim 10 with:

10. The computing device of claim 1, wherein the platform detection module is further adapted to activate the platform dependent module of the plurality of platform dependent modules that is capable of running on the targeted platform.

Please replace claim 15 with:

15. A computer comprising:

data;

error detection data;

indication means for indicating an error detection scheme;

detection means for detecting a current platform;

processing means for processing the data while running on the current platform;

error detection means for detecting an error in the data using the error detection scheme and the error detection data, the error detection means activated by the processing means; and

providing means for executing or manipulating the data if the error detection means does not detect an error in the data, wherein the providing means comprises one or more of:

secondary checking means for performing a secondary error check; equivalency checking means for performing an equivalency check with respect to information of the data and with reference to an expected value; installation means for installing a file of the data; retransmission means for retransmitting the file of the data; cryptographic means for encrypting and/or decrypting the data; compression/decompression means for compressing and/or decompressing the data; and alteration means for altering the information of the data.

Please replace claim 16 with:

16. The computer as recited in claim 15, wherein the arrangement comprises at least one of (i) an electronic device and (ii) one or more electronically-accessible media comprising electronically-executable instructions.

Please replace claim 21 with:

21. A computer, comprising:

 a processor coupled to a memory storing a data component and error detection data associated with the data component, wherein the processor executes:

 a platform detection component adapted to detect a current platform,

 an error detection selection component holding a selected error detection scheme,

 a plurality of modules capable of running on at least two different platforms, each module of the plurality of modules adapted to apply the selected error detection scheme on the data component to detect errors; at least one module of the plurality of modules activated by the platform detection component, the at least one module being activated to run on the current platform, and

 a transference success module capable of running on the current platform and adapted to execute or manipulate contents of the data component, wherein the plurality of modules include a plurality of platform dependent modules that may be activated by the platform detection component and a plurality of error detection modules that are adapted to apply the selected error detection scheme on the data component to detect errors.

Please replace claim 23 with:

23. The computer of claim 1, wherein the processor further executes: a transference success module running on the targeted platform and verifying the data is executable and not corrupt.

REASONS FOR ALLOWANCE

Claims 1-7, 10, 15, 16, 21 and 23 are allowed. The following is an Examiner's statement of reasons for allowance:

Independent claim 1 of the present application teaches a computing device comprising: a memory for storing data and error detection data associated with the data; and a processor coupled to the memory, wherein the processor executes: a plurality of platform dependent modules, each platform dependent module-capable of running on a different platform and processing the data, a platform detection module to (1) detect a targeted platform on which a data transference package is running and (2) activate one of the plurality of platform dependent modules to run on the targeted platform, an error detection selection module that holds a selected error detection scheme, and an error detection module running on the targeted platform to activate the selected error detection scheme with respect to the data, wherein the targeted platform includes at least one of a targeted operating system (OS) and a targeted computing environment. The foregoing limitations are not found in the prior arts of record. Particularly, none of the prior arts of record teach nor fairly suggest, "...a plurality of platform dependent modules, each platform dependent module-capable of running on a different platform and processing the data, a platform detection module to (1) detect a targeted platform on which a data transference package is running and (2) activate one of the plurality of platform dependent modules to run on the targeted platform, an error detection selection module that holds a selected error detection scheme, and an error detection module running on the targeted platform to activate the selected error detection scheme with respect to the data, wherein the targeted

platform includes at least one of a targeted operating system (OS) and a targeted computing environment.”

Dependent claims 2-7, 10 and 23 depend from allowable independent claim 1 and inherently include limitations therein and therefore are allowed as well.

Independent claim 15 teaches a computer comprising: data; error detection data; indication means for indicating an error detection scheme; detection means for detecting a current platform; processing means for processing the data while running on the current platform; error detection means for detecting an error in the data using the error detection scheme and the error detection data, the error detection means activated by the processing means; and providing means for executing or manipulating the data if the error detection means does not detect an error in the data, wherein the providing means comprises one or more of: secondary checking means for performing a secondary error check; equivalency checking means for performing an equivalency check with respect to information of the data and with reference to an expected value; installation means for installing a file of the data; retransmission means for retransmitting the file of the data; cryptographic means for encrypting and/or decrypting the data; compression/decompression means for compressing and/or decompressing the data; and alteration means for altering the information of the data. The foregoing limitations are not found in the prior arts of record. Particularly, none of the prior arts of record teach nor fairly suggest, “...the error detection means activated by the processing means; and providing means for executing or manipulating the data if the error detection means does not detect an error in the data, wherein the providing means comprises one or more of: secondary checking means for performing a secondary error check; equivalency checking means for performing an equivalency

check with respect to information of the data and with reference to an expected value; installation means for installing a file of the data; retransmission means for retransmitting the file of the data; cryptographic means for encrypting and/or decrypting the data; compression/decompression means for compressing and/or decompressing the data; and alteration means for altering the information of the data.”

Dependent claim 16 depends from allowable independent claim 15 and inherently includes limitations therein and therefore is allowed as well.

Independent claim 21 teaches a computer comprising: a processor coupled to a memory storing a data component and error detection data associated with the data component, wherein the processor executes: a platform detection component adapted to detect a current platform, an error detection selection component holding a selected error detection scheme, a plurality of modules capable of running on at least two different platforms, each module of the plurality of modules adapted to apply the selected error detection scheme on the data component to detect errors; at least one module of the plurality of modules activated by the platform detection component, the at least one module being activated to run on the current platform, and a transference success module capable of running on the current platform and adapted to execute or manipulate contents of the data component, wherein the plurality of modules include a plurality of platform dependent modules that may be activated by the platform detection component and a plurality of error detection modules that are adapted to apply the selected error detection scheme on the data component to detect errors. The foregoing limitations are not found in the prior arts of record. Particularly, none of the prior arts of record teach nor fairly suggest, “...a plurality of modules capable of running on at least two different platforms, each module of

the plurality of modules adapted to apply the selected error detection scheme on the data component to detect errors; at least one module of the plurality of modules activated by the platform detection component, the at least one module being activated to run on the current platform, and a transference success module capable of running on the current platform and adapted to execute or manipulate contents of the data component, wherein the plurality of modules include a plurality of platform dependent modules that may be activated by the platform detection component and a plurality of error detection modules that are adapted to apply the selected error detection scheme on the data component to detect errors.”

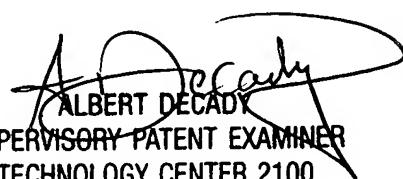
Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mujtaba K. Chaudry whose telephone number is 571-272-3817. The examiner can normally be reached on Mon-Thur 9-7:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert DeCady can be reached on 571-272-3819. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


Mujtaba Chaudry
Art Unit 2133
December 4, 2006


ALBERT DECADY
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100